



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Michael H. Perrott

Title: METHOD AND APPARATUS FOR BIT ERROR RATE DETECTION

Application No.: 09/888,708 Filed: June 25, 2001

Examiner: Lamarre, Guy J. Group Art Unit: 2133

Atty. Docket No.: 026-0015 Confirmation No.: 8814

November 12, 2004

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AFTER APPEAL (37 C.F.R. § 1.116)

This paper is being submitted following the Notice of Appeal mailed on September 16, 2004, and coincident with a Request for Continued Examination (RCE) (37 C.F.R. § 1.114). In light of the Amendments and/or Remarks herein, further examination is requested.

11/17/2004 JADDO1 00000041 09888708

02 FC:1202 108.00 OP

11/17/2004 JADDO1 00000044 09888708

02 FC:1202 108.00 OP